PTO/SB/08A (10-01)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary) Sheet 2

Complete if known				
Application Number	10/672,961			
Filing Date	September 26, 2003			
First Named Inventor	Glenn J. Leedy			
Art Unit	2822			
Examiner Name	Monica Lewis			
Attorney Docket Number	ELM/002 Cont. 4			

	U.S. PATENT DOCUMENTS						
Examiner	Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents	Pages, Columns, Lines, Where Relevant Passages or Relevant		
initials*		Number - Kind Code ² (if known)			Figures Appear		
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		. 6,563,224	05/13/2003	Leedy			

	FOREIGN PATENT DOCUMENTS							
Examiner Cite initials No.1		Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents	Pages, Columns, Lines, Where Relevant Passages or Relevant	T⁰		
		JP 01-157561 A	06/20/1989	LSI Logic Corp.	Figures Appear			
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Substitu	Substitute for form 1449/PTO			Complete if known		
				Application Number	10/672,961	
INFORMATION DISCLOSURE			CLOSURE	Filing Date	September 26, 2003	
STATEMENT BY APPLICANT		First Named Inventor	Glenn J. Leedy			
		Art Unit	2822			
(use as many sheets as necessary)			necessary)	Examiner Name	Monica Lewis	
Sheet	2	of	2	Attorney Docket Number	ELM/002- Cont. 4	

	NON PATENT LITERATURE DOCUMENTS				
Examiner initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			
		S. Wolf, Silicon Processing, 1990, Lattice Press, Volume 2, Page 111.			
		Wolf, Stanley and Richard N. Tauber; Silicon Procesing For the VLSI Era, Volume 1: Process Technology; Sunset Beach, CA: Lattice Press, 1986, pp. 191-194.			
		S. Wolf, Silicon Processing For the VLSI Era, 1990, Lattice Press, Volume 2, page 191.			

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